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jc804 U.S. PTO

Applicant: D'Elia et al.  
Docket: AMDA.474PA  
Title: CVD GAS INJECTOR AND METHOD THEREFOR

jc571 U.S. PTO  
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CERTIFICATE UNDER 37 CFR 1.10

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- ☒ Transmittal sheet containing Certificate under 37 CFR 1.10.
- ☒ Patent Application: Pages Numbered 1-18; 30 claims; Abstract 1 pgs.
- ☒ 4 sheets of informal drawings
- ☒ An unexecuted Declaration
- ☐ Assignment of the invention to \_\_\_\_\_ Recordation Form Cover Sheet
- ☒ Please DEFER PAYMENT OF THE FILING FEE
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Basic Filing Fee = \$690.00

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Fee for total number of independent claims in excess of 3 =  $\$78 * (4 - 3) = \$78.00$

Total fee due = \$948.00

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## **CVD GAS INJECTOR AND METHOD THEREFOR**

### **Field of the Invention**

The present invention relates generally to chemical vapor deposition (CVD) and,  
5 more particularly, to semiconductor devices and their manufacture involving the  
deposition of a coating on an integrated circuit die.

### **Background of the Invention**

The semiconductor industry has recently experienced technological advances  
10 that have permitted dramatic increases in circuit density and complexity, and equally  
dramatic decreases in power consumption and package sizes. Present semiconductor  
technology now permits single-chip microprocessors with many millions of transistors,  
operating at speeds of hundreds of millions of instructions per second to be packaged in  
relatively small, air-cooled semiconductor device packages. These technological  
15 advances have fueled an increased demand for semiconductor devices and products that  
employ semiconductor devices. A byproduct of this increased demand has been a need  
to manufacture semiconductor devices having uniform circuit dimensions in an efficient  
manner as considerations including device speed, reliability and affordability become  
increasingly important.

20 One example manufacturing process used in the manufacture of semiconductor  
devices that affects such considerations includes chemical vapor deposition (CVD). In  
a typical CVD process, reactant gas is introduced to a chamber containing a  
semiconductor wafer. The gas is decomposed and reacted at a surface of the wafer to

form a thin film of material such as silicon nitride, silicon dioxide or polycrystalline silicon. Many variations in the CVD processing can be used to achieve selected purposes. For more information regarding CVD, reference may be made to 1 S. WOLF & R.N. TAUBER, SILICON PROCESSING FOR THE VLSI ERA - PROCESS TECHNOLOGY 161-  
5 197 (1986).

CVD and other film deposition processes have been useful for various types of processes including semiconductor fabrication. However, the demands for improved products continue to present challenges to these and other processes. For example, as semiconductor die circuitry and other structures are scaled to a submicron scale,  
10 controlling their dimensions becomes increasingly challenging. One manner in which semiconductor structures are formed includes depositing a film on a semiconductor wafer to be used in controlling the structure dimensions. However, forming films having uniform thickness and/or other properties can be difficult.

In the CVD process, thickness, refractive index, extinction coefficient and other  
15 film properties are dependent upon deposition conditions including the concentration, residence time and pressure at which materials are supplied to the deposition process. These conditions are dependent upon the CVD process and the chamber characteristics. In some instances, chamber characteristics make it difficult to obtain a uniform gas supply.

20 For example, as reaction products are pumped from a CVD chamber during semiconductor wafer manufacturing processes, the pumping action may tend to draw reaction gasses from portions of the chamber in an uneven manner, which in turn affects the deposition conditions. When certain portions of the CVD chamber experience

insufficient gas supply, these portions, sometimes including half or more of the chamber, can become unsuitable for CVD. This often means that fewer semiconductor wafers can be processed in the chamber, thereby decreasing output and adding time, materials, labor and overall expense to the wafer manufacturing process. Using existing  
5 CVD processes and systems, maintaining a uniform supply of gas over the surface upon which a film is being formed and maintaining uniform process conditions can be difficult or even impossible.

### **Summary of the Invention**

10 The present invention is directed to the deposition of a film upon a semiconductor wafer, and is exemplified in a number of implementations and applications, some of which are summarized below.

According to an example embodiment of the present invention, a chemical-vapor deposition (CVD) gas injector is adapted to supply gas to a CVD chamber in a  
15 manner that enhances the properties of deposited films. The injector has a gas inlet coupled to a gas source and supplies gas from the source to the CVD system via at least one gas outlet. The outlet shape and injector size are adapted to deliver supply gas in a manner that sufficiently maintains uniform delivery of the gas in a zone of the CVD system that would exhibit a depleted supply gas concentration absent the injector.

20 According to another example embodiment of the present invention, a CVD system is adapted to deposit a coating on a surface of a semiconductor wafer in a deposition chamber in the CVD system. The system includes a gas source and a gas injector adapted to deliver supply gas from the source to the CVD chamber through one

or more gas outlets. The CVD chamber generates conditions suitable for CVD of the coating. An exhaust pump is coupled to the CVD chamber and adapted to pump gas including reaction byproducts from the chamber. The pumping of these gases can cause a depletion of the supply gas in regions of the chamber. The gas injector is adapted to  
5 compensate for the depletion of supply gas so that the surface of the wafer receives gas at uniform delivery and pressure during formation of the wafer coating.

The above summary of the present invention is not intended to describe each illustrated embodiment or every implementation of the present invention. The figures and detailed description that follow more particularly exemplify these embodiments.

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#### **Brief Description of the Drawings**

The invention may be more completely understood in consideration of the following detailed description of various embodiments of the invention in connection with the accompanying drawings, in which:

15

FIG. 1 is a CVD system employing a gas injector, according to an example embodiment of the present invention;

FIG. 2 is a three-port gas injector, according to another example embodiment of the present invention;

20

FIG. 3 is a five-port gas injector, according to another example embodiment of the present invention; and

FIG. 4 is a gas injector having a variable outlet, according to another example embodiment of the present invention.

While the invention is amenable to various modifications and alternative forms, specifics thereof have been shown by way of example in the drawings and will be described in detail. It should be understood, however, that the intention is not necessarily to limit the invention to the particular embodiments described. On the  
5 contrary, the intention is to cover all modifications, equivalents, and alternatives falling within the spirit and scope of the invention as defined by the appended claims.

### **Detailed Description**

The present invention is believed to be applicable for a variety of different types  
10 of CVD processes, and the invention has been found to be particularly suited for CVD of a uniform coating on a semiconductor wafer. While the present invention is not necessarily limited to such applications, various aspects of the invention may be appreciated through a discussion of various examples using this context.

According to an example embodiment of the present invention, a gas injector is  
15 adapted to deliver gas to a CVD arrangement in a manner that facilitates uniform supply of the gas over a surface having a coating formed using CVD. The uniform gas supply is sufficiently maintained to enhance the ability to form coatings having uniform properties, such as thickness, refractive index, and reflectivity. These and other properties depend upon the residence time of the gasses present with the surface being  
20 coated. Maintaining a sufficiently uniform gas supply improves the residence time, allowing the supplied gas, combined with selected reactants and conditions, to improve the quality of the deposited film.

The injector is useful in various applications, including systems and methods for forming a coating on a semiconductor wafer, and is particularly useful for low-pressure CVD (LPCVD) applications including thermal applications. For example, a typical thermal LPCVD process involves supplying a reactant gas to wafer surfaces in a deposition chamber. As the supplied gas passes through the chamber towards the exhaust, some of the gas is reacted, resulting in a decreasing amount of reactant gas and an increasing amount of reactant byproducts. This results in a depleted supply gas concentration and, therefore, delivery of less supply gas to wafers located in regions of the chamber near the exhaust. The injector is adapted to compensate for the depleted concentration of supply gas by supplying additional gas to depleted regions. The wafer coating can then be used for subsequent processing, such as for dimensioning circuitry and other structure in the wafer. The uniform distribution of the gas and related uniformity of the coating improve the ability to control subsequent processing, such as photolithography.

Sufficiently maintaining a uniform supply of gas in the CVD arrangement can provide various advantages. For example, the gas concentration can be maintained in a manner that provides a sufficient amount of gas available for reaction at a wafer surface. The provision of sufficient reactant gas makes possible the formation of a coating or coatings having uniform optical properties, such as the index of refraction (n value), extinction coefficient (k value), reflectivity and thickness-related properties. The extinction coefficient represents a measure of how much light is absorbed by the coating. In one implementation, the coating is formed having an extinction coefficient of between about 0.6 and 0.8. In another implementation, the refractive index of the

coating, or n value, is maintained between about 2.3 and 2.5, and in another implementation, the n value throughout the coating has a variation of less than about 20%. In still another implementation, the gas concentration is sufficiently maintained to form a coating having a thickness that has a variation of less than about 6%.

5           In one particular example embodiment of the present invention, a quartz injector is adapted for supplying gas (*e.g.*, ammonia (NH<sub>3</sub>), dichlorosilane or other reactants) to a LPCVD process. A silicon rich nitride optical anti-reflective coating is deposited on a semiconductor wafer in a CVD reactor. The coating is useful, for example, to control Polysilicon gate dimensions in the manufacture of high speed, high performance  
10   integrated logic circuits. The injector makes possible the uniform delivery of and the development of a uniform pressure field for high purity ammonia or other selected reactant in the CVD reactor. One advantage of this injector device is gained by the ability to deliver ammonia to zones in the reactor that are, using existing technology, currently depleted in ammonia due to the reaction of the ammonia in previous zones.  
15   As a result, optical properties, such as the refractive index and k values, of the silicon rich nitride film deposited are much more uniform across the reactor. Therefore, control of gate dimensions of polysilicon and other features formed during subsequent operations is improved.

FIG. 1 is an example CVD arrangement 200 that addresses depletion problems  
20   including those discussed in connection with the Background hereinabove, according to an example embodiment of the present invention. Semiconductor wafers 220 are located in the device, and a gas is supplied via the injector 210. Gas including reactant byproducts is exhausted by an exhaust pump as indicated by arrows 235. The



exhaustion of the gas results in an increased velocity of gas being removed and would, absent the injector, form depletion regions in the device and cause problems in the CVD process. For example, without the injector, coatings formed using CVD on the wafers located near the exhaust may exhibit properties different from those wafers located  
5 away from the exhaust. In addition, individual wafers may exhibit variations in properties due to an uneven distribution of gas over the wafer.

The injector 210 has injection outlets 215 and is adapted to provide a uniform supply of gas to the wafers 220. The injection outlets are selected to make up for regions of the CVD arrangement that would exhibit gas depletion, absent the injector. It  
10 has been discovered that, by supplying gas using the injection outlets 215, a uniform supply of gas is supplied to the wafers, making possible the application of CVD coatings on the wafers 220 that exhibit uniform properties.

FIG. 2 shows a more particular example embodiment of the present invention including an injector 300 adapted to maintain a uniform gas supply to a CVD  
15 arrangement, such as that shown in FIG. 1. The injector has an inlet 310 and a plurality of outlets 320, 322, and 324. Gas is supplied to the inlet 310, through a supply tube 315 and out of the outlets into a CVD arrangement. The shape of the supply tube and each outlet is selected to enhance the uniformity gas supplied to a surface or surfaces of semiconductor wafers undergoing CVD processing.

20 In one example implementation, the injector in FIG. 2 is used in the CVD arrangement of FIG. 1, and has the following characteristics: the supply tube has a diameter of between about 7mm and 9mm and extends into the CVD arrangement to

about 950mm; the outlets 320, 322 and 324 are located at distances of about 320mm, 620mm and 940mm from the entrance 212 to the CVD arrangement.

Uniform gas supply is made possible because of the outlet selection on the injector where gas is delivered from the outlet or outlets at points in the reactor where gas, such as ammonia, is depleted. The present invention is particularly useful for controlling the supply of reactant gas, such as ammonia or dichlorosilane, to LPCVD processes involving the deposition of a coating on one or more semiconductor wafers. For example, in many CVD processes, ammonia is a primary reactant that controls coating properties including refractive index and k optical values. Since the system operates under low pressure, the reactor zone that is closest to the vacuum port develops an increased velocity of reactant gas and therefore reduces the residence time of the reactant gas or gasses at the surface. In addition, the gas reaching the wafers near the exhaust commonly has a lower concentration of reactant gas due to reactions occurring with other wafers. This depletion in gas supply and subsequent reduction in residence time adversely affects optical properties, (e.g. n, k parameters). This method of injection increases reactant gas supply in depleted zones, thereby raising the residence time of the reactant at the surface and hence slows the depletion affects, permitting uniform thickness and optical properties.

In another example embodiment of the present invention, the injector is positioned in the CVD arrangement so that the outlet or outlets face the reactor tube wall. Referring again to FIG. 1, the outlets shown face a wall 230 of the CVD arrangement. It has been discovered that, by arranging the outlets so that they face the reactor tube wall, surface defects on the surface of wafers being processed are reduced.

Various other positions and arrangements of the injector in a CVD arrangement can be used. For example, a plurality of injectors can be placed in a CVD system. By using more than one injector, the uniformity of the gas supplied may be enhanced. Referring again to FIG. 1, one or more injectors may be inserted into different portions  
5 of the CVD arrangement, such as in region 240. In addition, the placement of the injector or injectors can be adapted for particular CVD arrangements, depending on the portions of the CVD reactors that exhibit gas depletion.

The shape, number, and location of injector outlets can also be selected to accommodate various CVD depletion areas. FIG. 3 shows one example implementation  
10 where the injector 400 includes five injector outlets 420, 422, 424, 426 and 428. FIG. 4 shows another example implementation where the injector 500 has an outlet 520 that varies in shape along the length of the injector. Various other combinations and implementations of outlet shapes consistent with the present invention may be selected to address depletion problems as needed.

15 In another example embodiment of the present invention, the outlets are adjustable. The adjustment of the outlets are selected based upon items such as the CVD chamber size, the process for which the injector is being used, the desired flow rate of the gas and the type of gas that is being supplied. Various parameters, such as temperature, density, and pressure of the gas; chamber size and shape; desired coating  
20 properties; type and quantity of substrate or wafers that are to be coated; and others can be accommodated by the adjustable outlets.

In another example embodiment of the present invention, one or more gas concentration detectors are used to detect the concentration of gas in the CVD chamber.

The detected concentration is used to select the shape of the injector outlets in a manner that improves the uniformity of gas at a surface or surfaces being coated. The adjustment of the outlets can be achieved in various manners, including using manual and automatic methods. In one implementation, the detector is communicatively  
5 coupled to a controller that is adapted to control the adjustment of the outlets. A feedback loop is used at the controller to adjust the outlets in response to the detected gas concentration. The feedback can be used during processing to dynamically monitor the gas concentration. The detector is placed in an area or areas of the CVD chamber for which gas concentration is to be monitored.

10 In one implementation, gas concentration detectors are used in the chamber prior to processing semiconductor wafers under simulated process conditions. The detectors are placed in selected locations to detect reactant concentrations, and the CVD chamber is operated under conditions similar to actual process conditions. Reactant gas is supplied and the resulting concentrations in selected portions of the chamber are  
15 detected. This can be accomplished in a manner that enables the use of conventional detectors, such as without performing deposition and without reaching process temperatures. The detected concentrations are then used to set parameters, such as the injector type, hole size or flow rate to enhance the CVD process.

While the present invention has been described with reference to several  
20 particular example embodiments, those skilled in the art will recognize that many changes may be made thereto without departing from the spirit and scope of the present invention, which is set forth in the following claims.

**What is claimed is:**

- 1 1. A chemical-vapor deposition (CVD) gas injector having a gas inlet coupled to a  
2 gas source and adapted to supply gas from the source to a CVD system via at least one  
3 gas outlet in a manner that sufficiently maintains uniform supply of the gas in a zone of  
4 the CVD system that would exhibit a depleted gas supply absent the injector.
- 1 2. The injector of claim 1, wherein the injector is adapted to supply gas to a CVD  
2 system zone that exhibits an increased velocity of reactant gas in the zone due to a  
3 vacuum port in the CVD system.
- 1 3. The injector of claim 1, wherein the injector is adapted to increase the residence  
2 time of the supply gas at the CVD system zone.
- 1 4. The injector of claim 1, wherein the injector is adapted to provide a uniform  
2 pressure field of the supply gas in the CVD system.
- 1 5. The injector of claim 1, wherein the at least one gas outlet includes a plurality of  
2 outlets, and wherein the shape of each outlet is selected to sufficiently maintain the  
3 uniform supply of the gas.
- 1 6. The injector of claim 5, wherein at least two of the plurality of outlets have a  
2 different shape.

1     7.     The injector of claim 1, wherein the shape of at least one gas outlet is adjustable.

1     8.     The injector of claim 7, further comprising:

2             a gas concentration detector adapted to detect the concentration of gas in the

3     CVD system; and

4             an outlet shape adjustment device adapted to adjust the shape of the at least one

5     adjustable gas outlet in response to the gas concentration detector.

1     9.     The injector of claim 1, wherein the injector is adapted to supply gas including

2     at least one of: ammonia and dichlorosilane.

1     10.    The injector of claim 1, wherein the maintained uniform gas supply is selected

2     for CVD of a silicon nitride antireflective coating on a semiconductor wafer.

1     11.    The injector of claim 1, wherein at least a portion of the injector includes quartz.

1     12.    A chemical-vapor deposition (CVD) system adapted to deposit a coating on a

2     surface of at least one semiconductor wafer, the system comprising:

3             a gas source;

4             a CVD reaction chamber adapted to hold the die, to receive CVD reaction gasses

5     from the gas source and to generate conditions that cause the gasses to react and form

6     the wafer coating;

7 an exhaust pump coupled to the CVD reaction chamber and adapted to pump  
8 exhaust from the chamber; and

9 a gas injector located in the reaction chamber and adapted to supply gas from the  
10 gas source to the surface of the at least one wafer at uniform supply and pressure during  
11 formation of the wafer coating.

1 13. The system of claim 12, wherein the gas injector includes the injector of claim 1.

1 14. The system of claim 12, wherein the exhaust pump causes gas to be drawn from  
2 the CVD reaction chamber in an uneven manner.

1 15. The system of claim 14, wherein the gas injector is adapted to compensate for  
2 the exhaust pump.

1 16. The system of claim 14, wherein the gas source is adapted to supply gas  
2 including ammonia to be used in the formation of a silicon-nitride anti-reflective  
3 coating on the wafer.

1 17. The system of claim 12, wherein the injector outlets face a wall in the CVD  
2 reaction chamber.

1 18. A system for forming a coating on a surface of a semiconductor wafer in a CVD  
2 arrangement, the system comprising:

May 17, 2000

3 means for supplying a uniform supply of gas to the surface of the wafer, the  
4 surface being in a zone of the CVD arrangement that exhibits a depleted gas supply  
5 absent the injector; and

6 means for using the supplied gas in combination with selected reactants to  
7 deposit a coating on the wafer.

1 19. A method for forming a coating on a surface of a semiconductor wafer in a CVD  
2 arrangement, the method comprising:

3 supplying gas to the surface of the wafer using a gas injector adapted to maintain  
4 uniform supply of the gas in a zone of the CVD arrangement that would exhibit a  
5 depleted gas supply absent the injector; and

6 using the supplied gas in combination with selected reactants and depositing a  
7 coating on the wafer.

1 20. The method of claim 19, wherein supplying gas to the surface includes  
2 supplying gas in different quantities to different zones of the CVD arrangement.

1 21. The method of claim 20, wherein the different quantities are selected to  
2 compensate for a gas depletion rate associated with the selected zone of the CVD  
3 arrangement to which the injector supplies gas.

1 22. The method of claim 19, wherein the gas includes at least one of: ammonia and  
2 dichlorosilane.



1 23. The method of claim 19, wherein depositing a coating on the wafer includes  
2 depositing an anti-reflective coating having uniform optical properties.

1 24. The method of claim 23, wherein the anti-reflective coating is deposited having  
2 a k value of refractive index that is between about 0.6 and 0.8.

1 25. The method of claim 23, further comprising performing photolithography on the  
2 wafer using the anti-reflective coating.

1 26. The method of claim 19, wherein depositing a coating on the wafer includes  
2 depositing a coating having uniform thickness.

1 27. The method of claim 19, further comprising adjusting the gas injector to  
2 maintain the uniform gas supply.

1 28. The method of claim 27, wherein adjusting the gas injector comprises:  
2 providing at least one gas concentration detector in the CVD arrangement;  
3 detecting the concentration of the supplied gas using the detector; and  
4 in response to the detected concentration, adjusting the gas injector.

1 29. The method of claim 28, prior to depositing a coating on the wafer, further  
2 comprising removing the at least one gas concentration detector from the CVD

3 arrangement after detecting the concentration of the supplied gas.

1 30. The method of claim 29, wherein detecting the concentration of the supplied gas  
2 using the detector includes operating the CVD arrangement under simulated processing  
3 conditions.

**Abstract**

Chemical vapor deposition (CVD) is enhanced by compensating for a depleted gas concentration zone in a CVD reactor. According to an example embodiment of the present invention, a chemical-vapor deposition (CVD) gas injector is adapted to supply  
5 gas to a CVD chamber in a manner that enhances the properties of deposited films. The injector has a gas inlet coupled to a gas source and supplies gas from the source to the CVD system via at least one gas outlet. The injector is adapted to deliver gas in a manner that sufficiently maintains uniform supply of the gas in a zone of the CVD system that would exhibit a depleted gas supply absent the injector. The uniform gas  
10 supply improves the CVD process in various manners, including making possible the deposition of films having uniform properties, such as reflectivity, extinction coefficient, thickness and refractive index.

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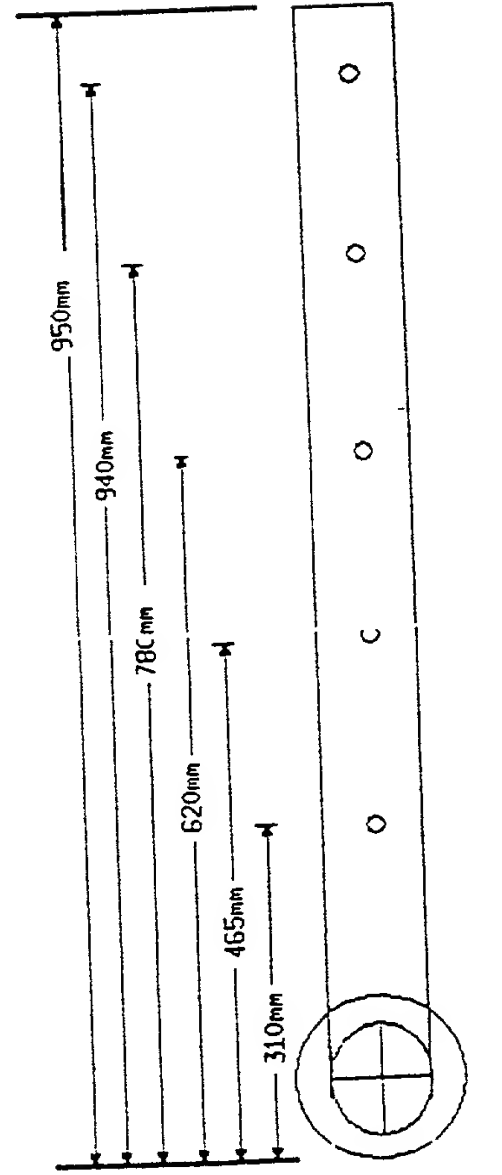
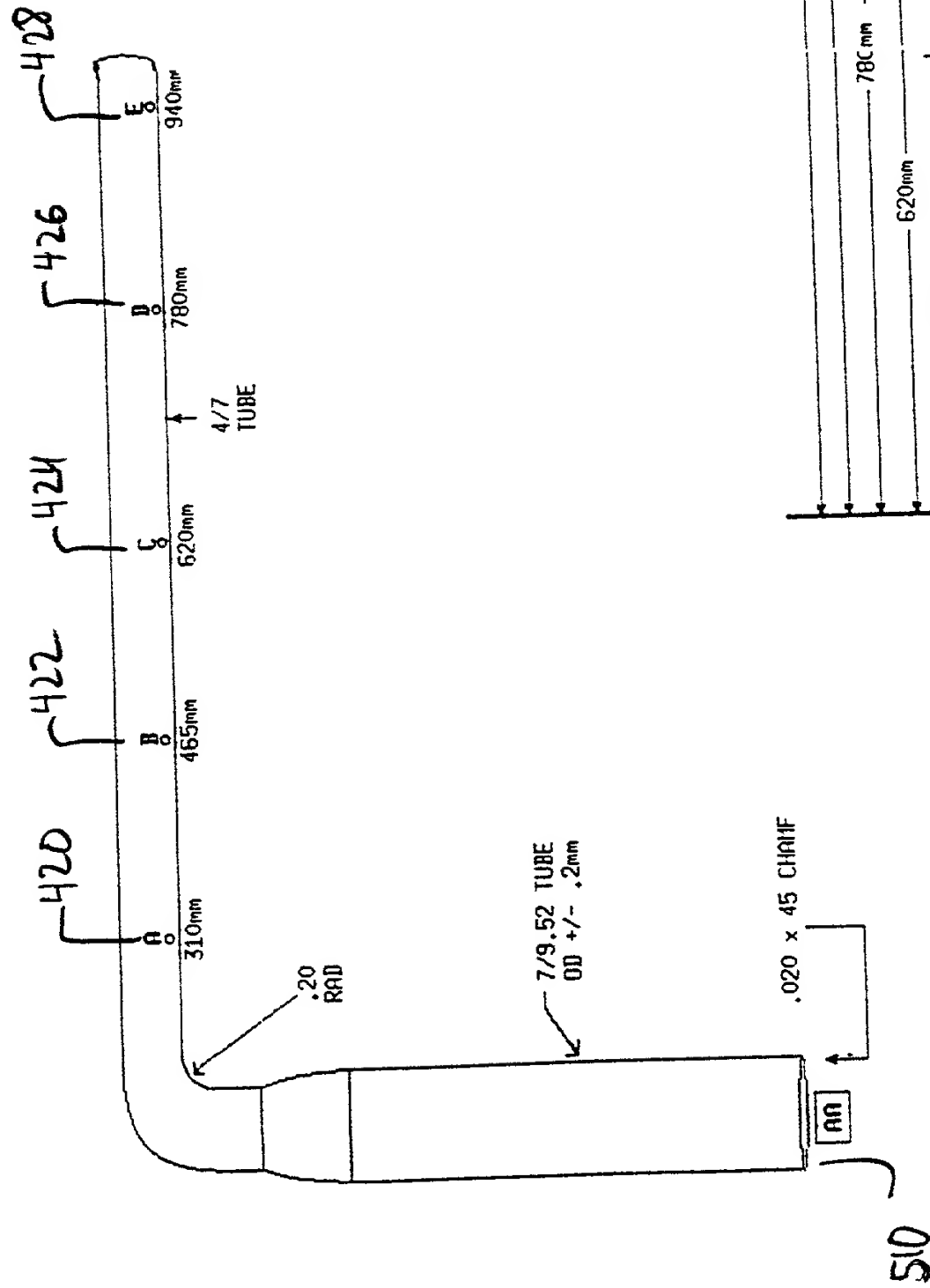
NH<sub>3</sub> INJECTION HOLES  
 AND MULTI PORT INJECTOR

2



Technical drawing of a mechanical part, likely a shaft or rod, showing dimensions in inches. The part has a circular cross-section at the left end and a tapered section. Dimensions are indicated by arrows and text:

- Overall length:  $3.29 \pm .02$
- Distance from the left end to the start of the tapered section:  $.32$
- Distance from the left end to the end of the tapered section:  $2.36$
- Two arrows pointing to the circular cross-section are labeled  $.00$ .



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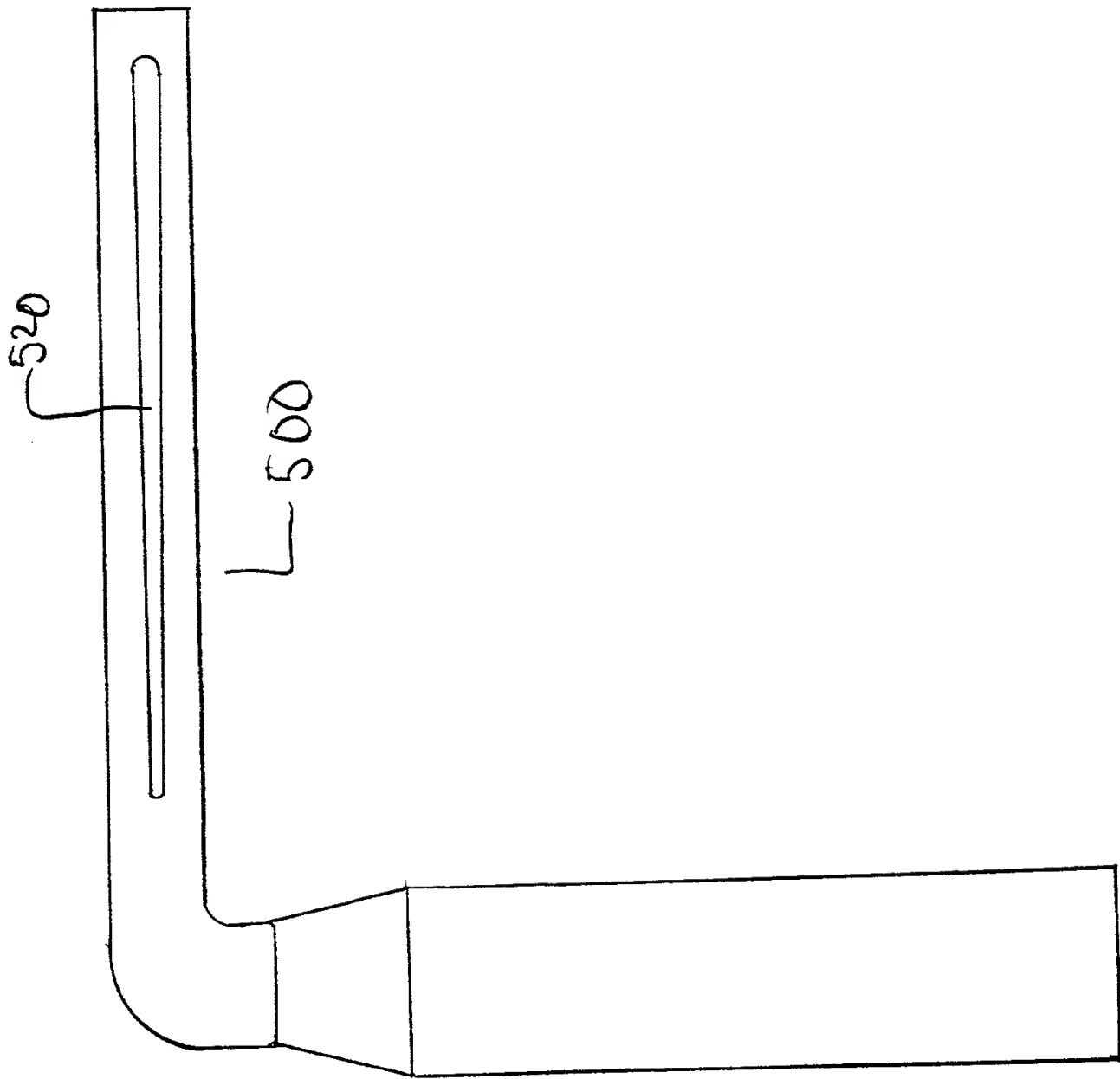


Fig. 4

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United States Patent Application

COMBINED DECLARATION AND POWER OF ATTORNEY

As a below named inventor I hereby declare that: my residence, post office address and citizenship are as stated below next to my name; that

I verily believe I am the original, first and sole inventor (if only one name is listed below) or a joint inventor (if plural inventors are named below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: **CVD Gas Injector and Method Therefor**.

The specification of which

- a. ☒ is attached hereto  
b. ☒ is entitled **CVD Gas Injector and Method Therefor**, having attorney docket number **AMDA.474PA (TT4037)**.  
c. ☐ was filed on \_\_\_\_\_ as application serial no. \_\_\_\_\_ and was amended on \_\_\_\_\_ (if applicable) (in the case of a PCT-filed application) described and claimed in international no. \_\_\_\_\_ filed \_\_\_\_\_ and as amended on \_\_\_\_\_ (if any), which I have reviewed and for which I solicit a United States patent.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, § 1.56 (attached hereto).

I hereby claim foreign priority benefits under Title 35, United States Code, § 119/365 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on the basis of which priority is claimed:

- a. ☒ no such applications have been filed.  
b. ☐ such applications have been filed as follows:

FOREIGN APPLICATION(S), IF ANY, CLAIMING PRIORITY UNDER 35 USC § 119			
COUNTRY	APPLICATION NUMBER	DATE OF FILING (day, month, year)	DATE OF ISSUE (day, month, year)
ALL FOREIGN APPLICATION(S), IF ANY, FILED BEFORE THE PRIORITY APPLICATION(S)			
COUNTRY	APPLICATION NUMBER	DATE OF FILING (day, month, year)	DATE OF ISSUE (day, month, year)

I hereby claim the benefit under Title 35, United States Code, § 120/365 of any United States and PCT international application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application.

U.S. APPLICATION NUMBER	DATE OF FILING (day, month, year)	STATUS (patented, pending, abandoned)

I hereby claim the benefit under Title 35, United States Code § 119(e) of any United States provisional application(s) listed below:

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I hereby appoint the following attorney(s) and/or patent agent(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected herewith:



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 Roddy, Richard J. Reg. No. 27,688  
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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

2 0 1	Full Name Of Inventor	Family Name D'ELIA	First Given Name MICHAEL	Second Given Name J.
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Signature of Inventor 201:			Date:	
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2 0 4	Full Name Of Inventor	Family Name BHAKTA	First Given Name JAYENDRA	Second Given Name D.
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Signature of Inventor 204:			Date:	